

FIG. 1

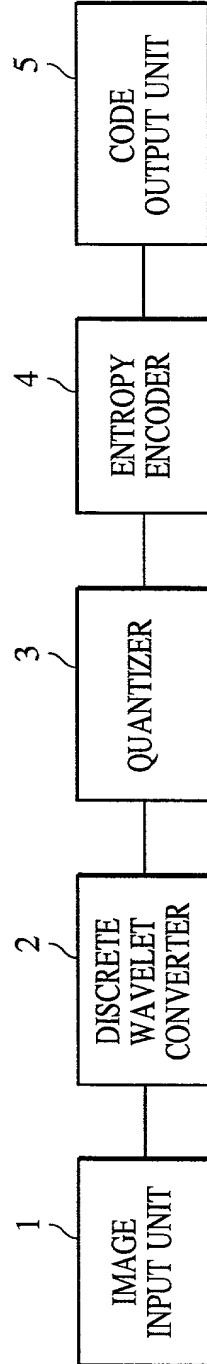


FIG. 2

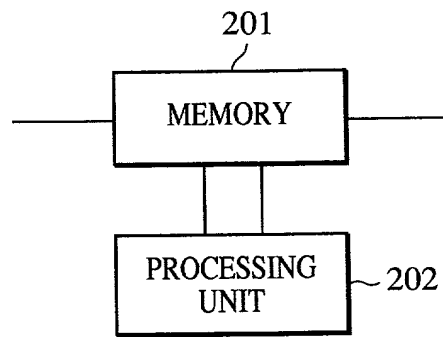


FIG. 3

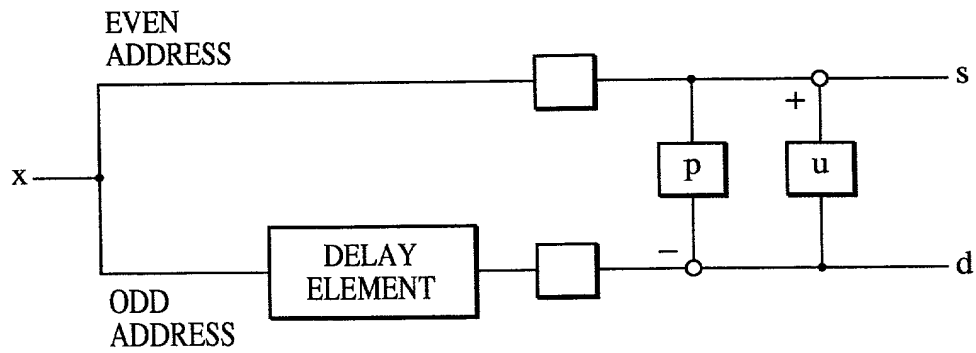


FIG. 4

LL	HL2	HL1
LH2	HH2	
LH1		HH1

FIG. 5A

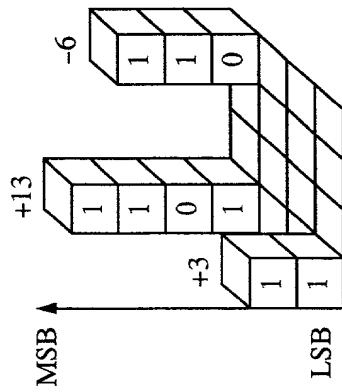


FIG. 5B

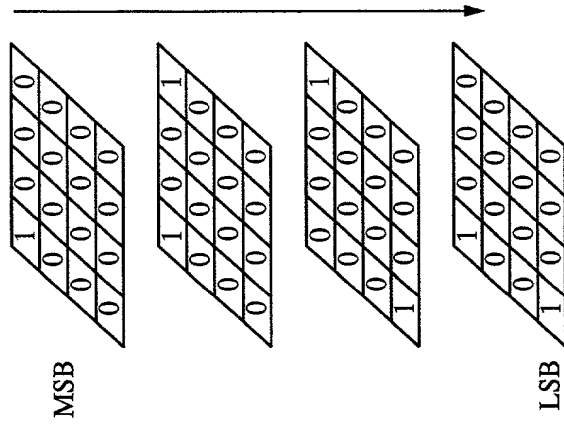


FIG. 6A

MH	TH0	BS0	TH1	BS1	...	THn-1	BSn-1
----	-----	-----	-----	-----	-----	-------	-------

FIG. 6B

IMAGE SIZE	TILE SIZE	NUMBER OF COMPONENTS	COMPONENT INFORMATION
---------------	--------------	-------------------------	--------------------------

FIG. 6C

TILE LENGTH	CODING PARAMETERS
----------------	----------------------

FIG. 6D

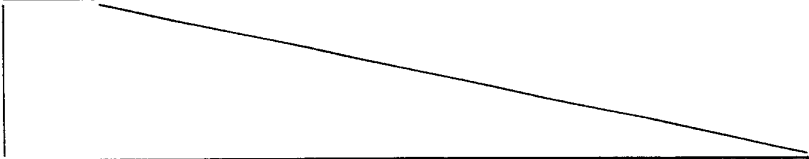
LL	HL2	LH2	HH2	HL1	LH1	HH1
						
Bit Plane S-1		Bit Plane S-2		...		Bit Plane 0

FIG. 7A

MH	TH0	BS0	TH1	BS1	...	THn-1	BSn-1
----	-----	-----	-----	-----	-----	-------	-------

FIG. 7B

IMAGE SIZE	TILE SIZE	NUMBER OF COMPONENTS	COMPONENT INFORMATION
---------------	--------------	-------------------------	--------------------------

FIG. 7C

TILE LENGTH	CODING PARAMETER
----------------	---------------------

FIG. 7D

Bit Plane S-1				Bit Plane S-2				Bit Plane 0			
LL	HL2	LH2	LL	...	LL	HL2	LH2	HL1	LH1	HH1	HH1

FIG. 8

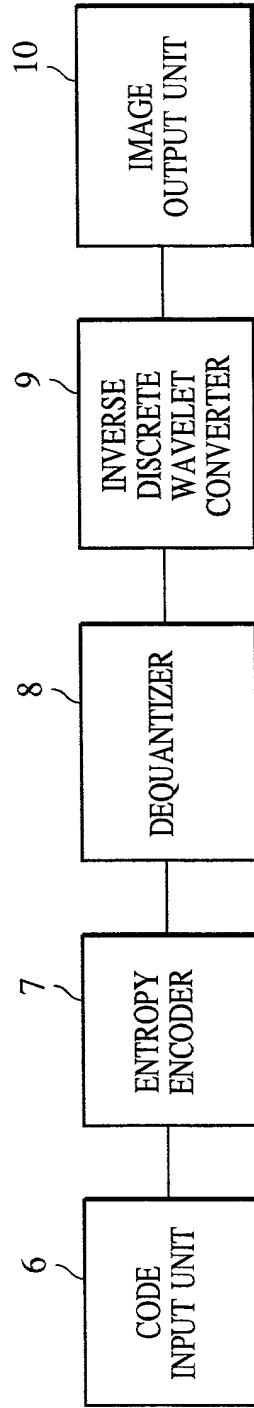


FIG. 9B

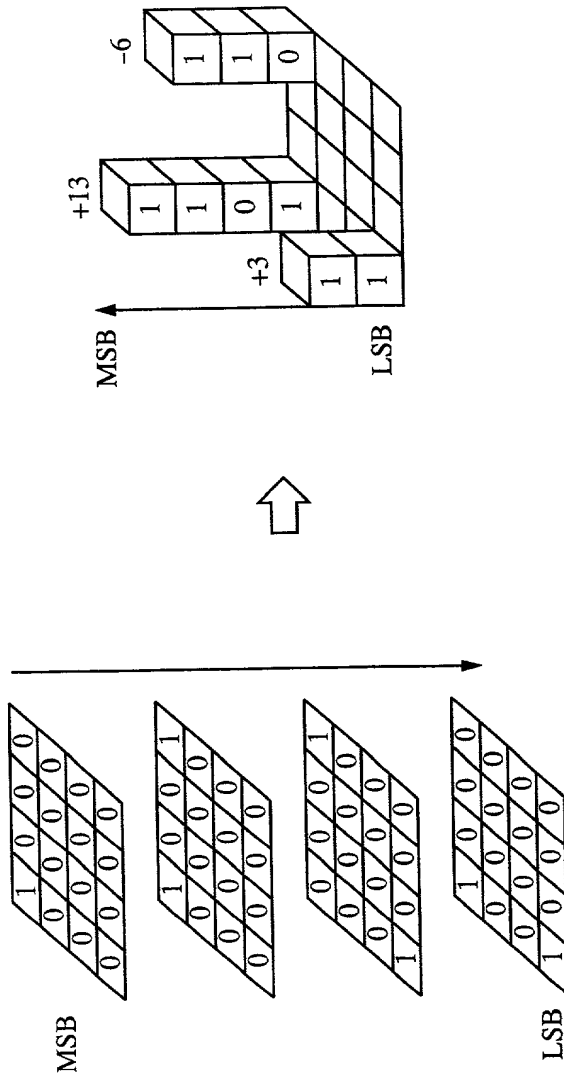




FIG. 10

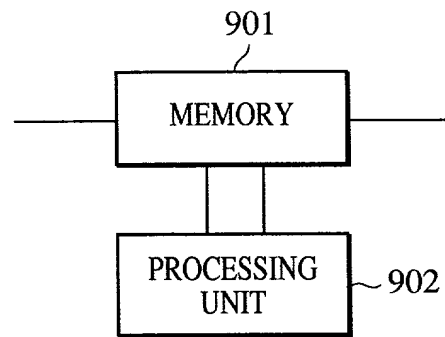


FIG. 11

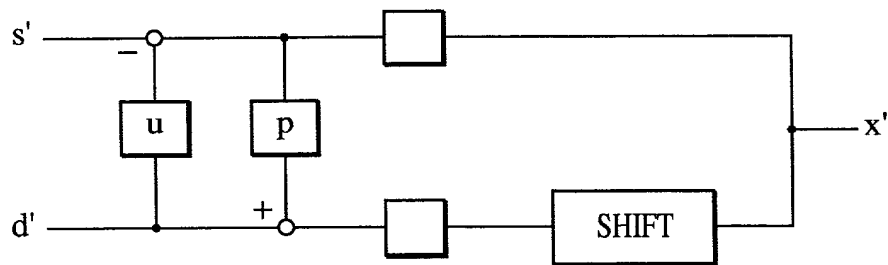


FIG. 12A

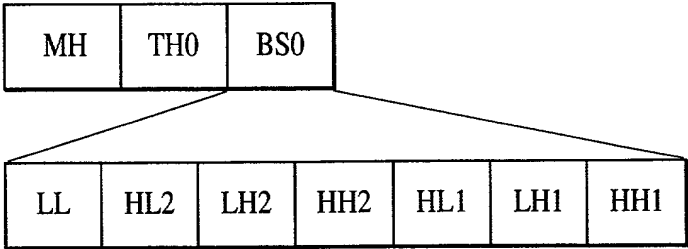


FIG. 12B

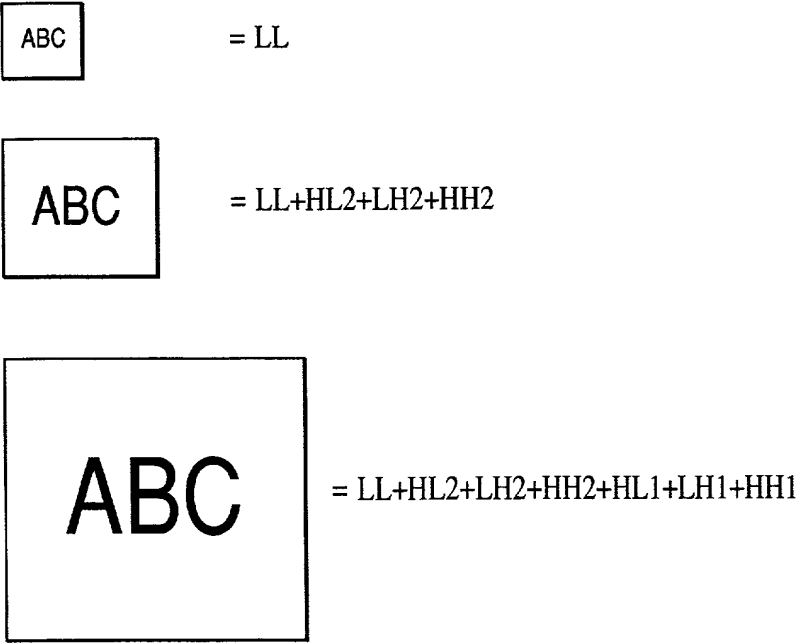


FIG. 13A

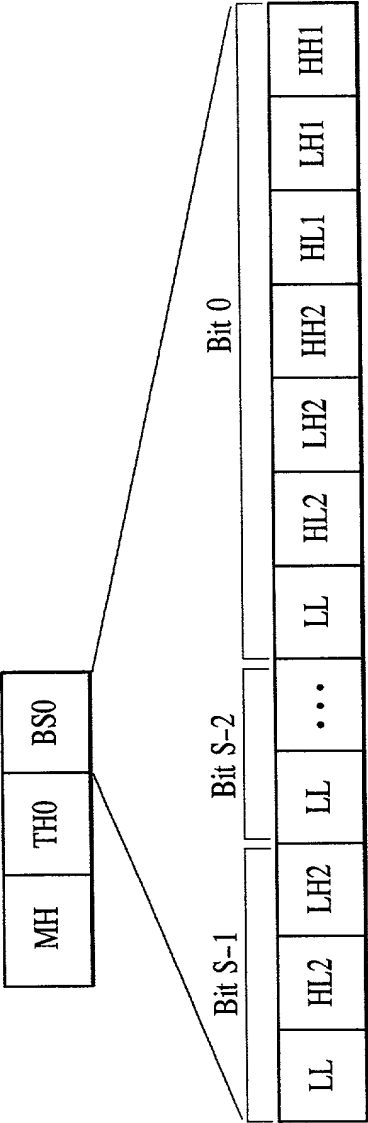
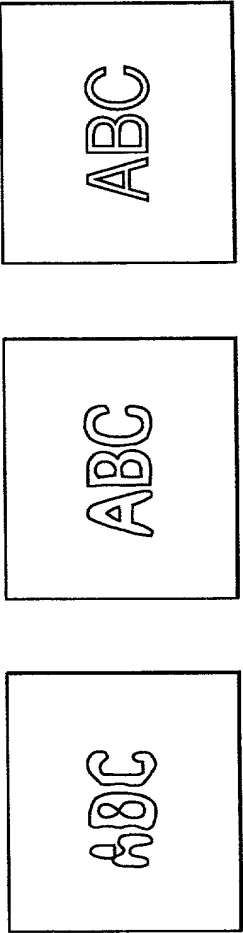


FIG. 13B



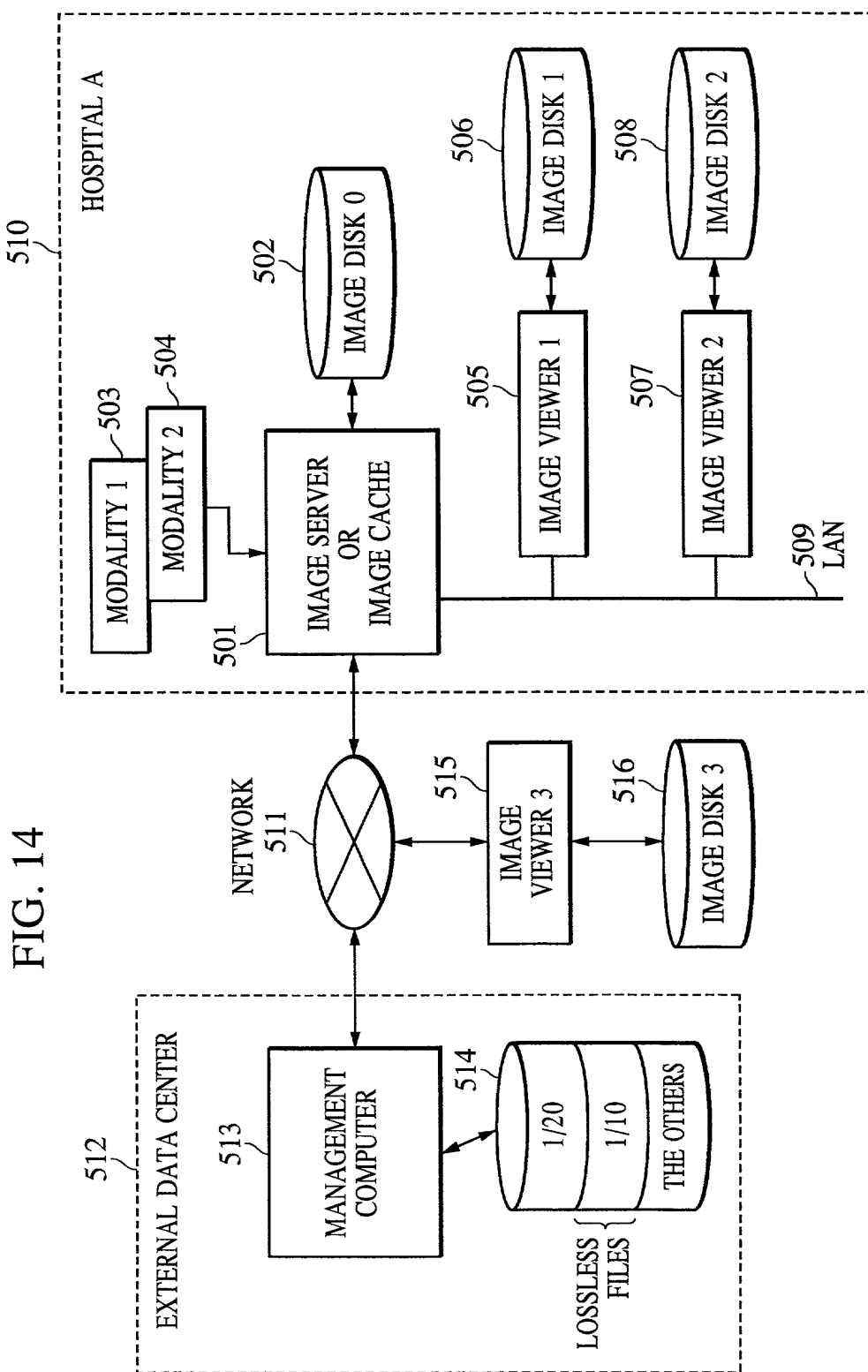


FIG. 14

FIG. 15

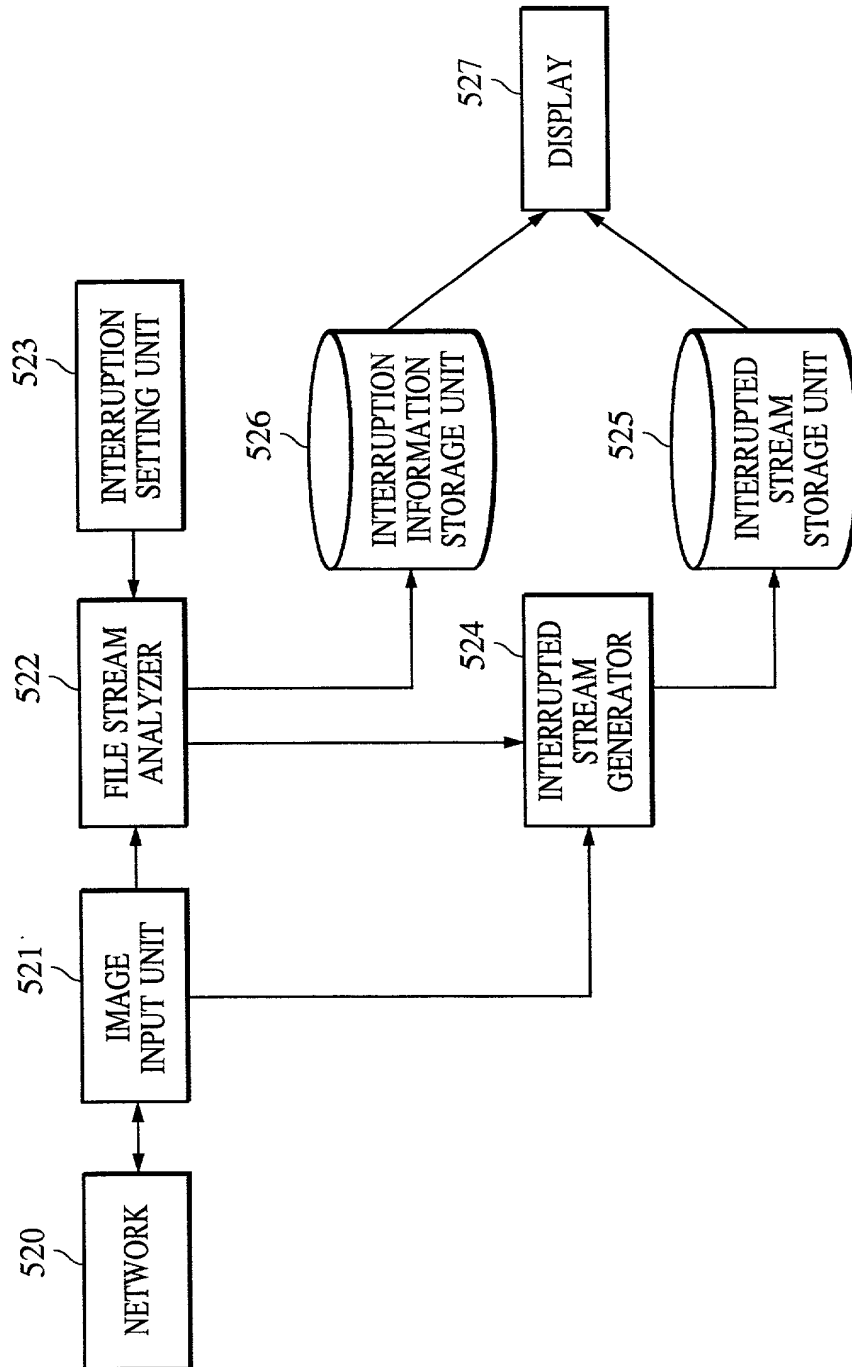


FIG. 16

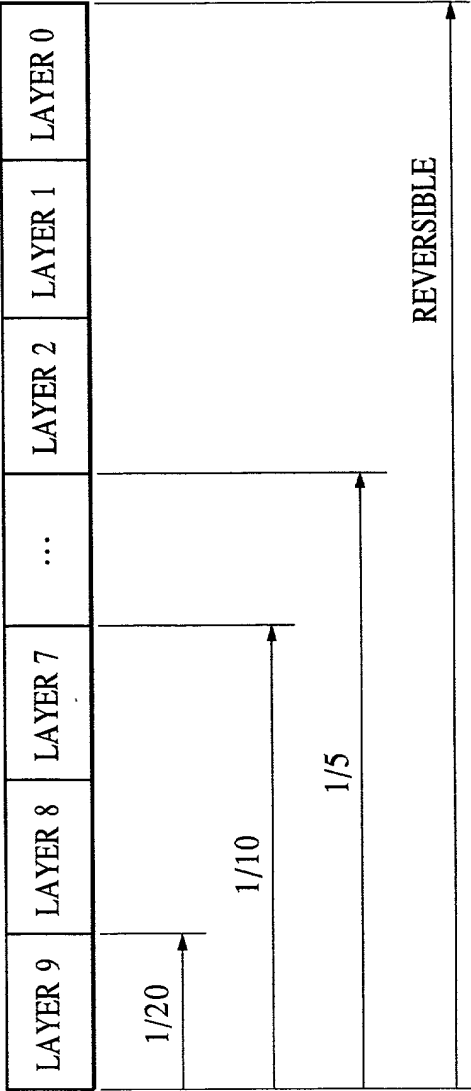


FIG. 17

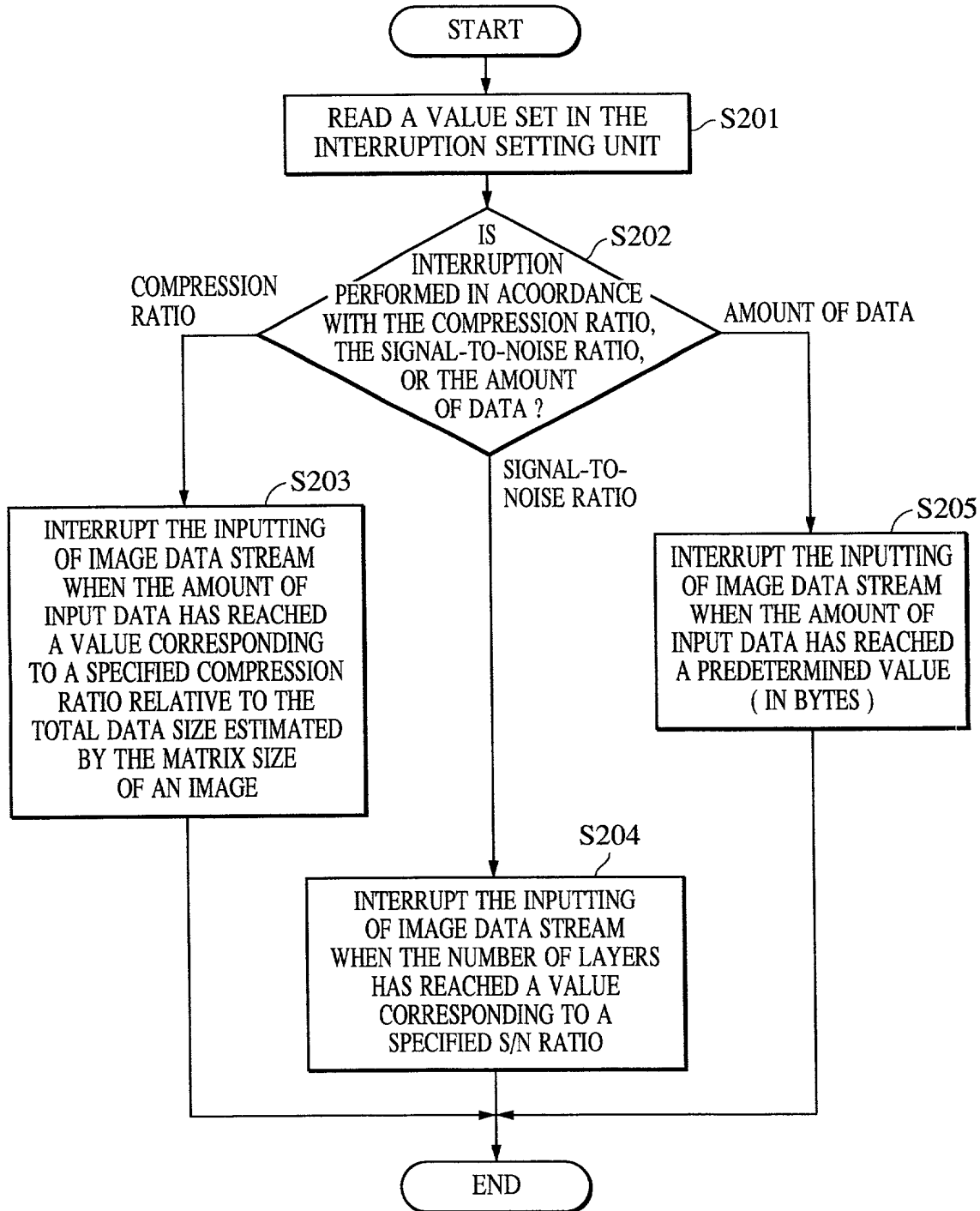


FIG. 18

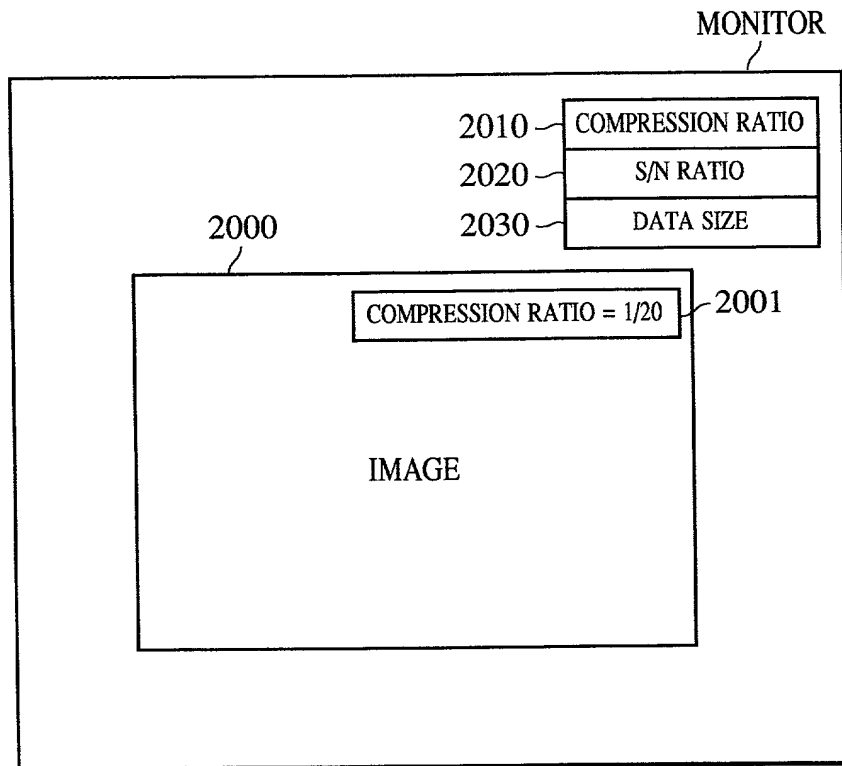
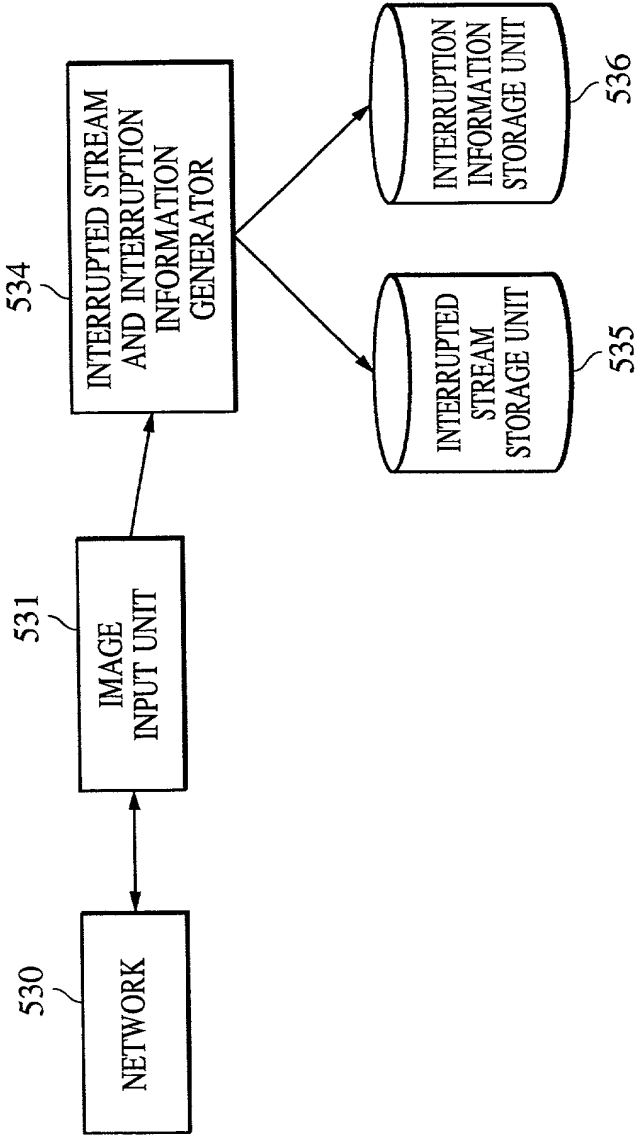




FIG. 19



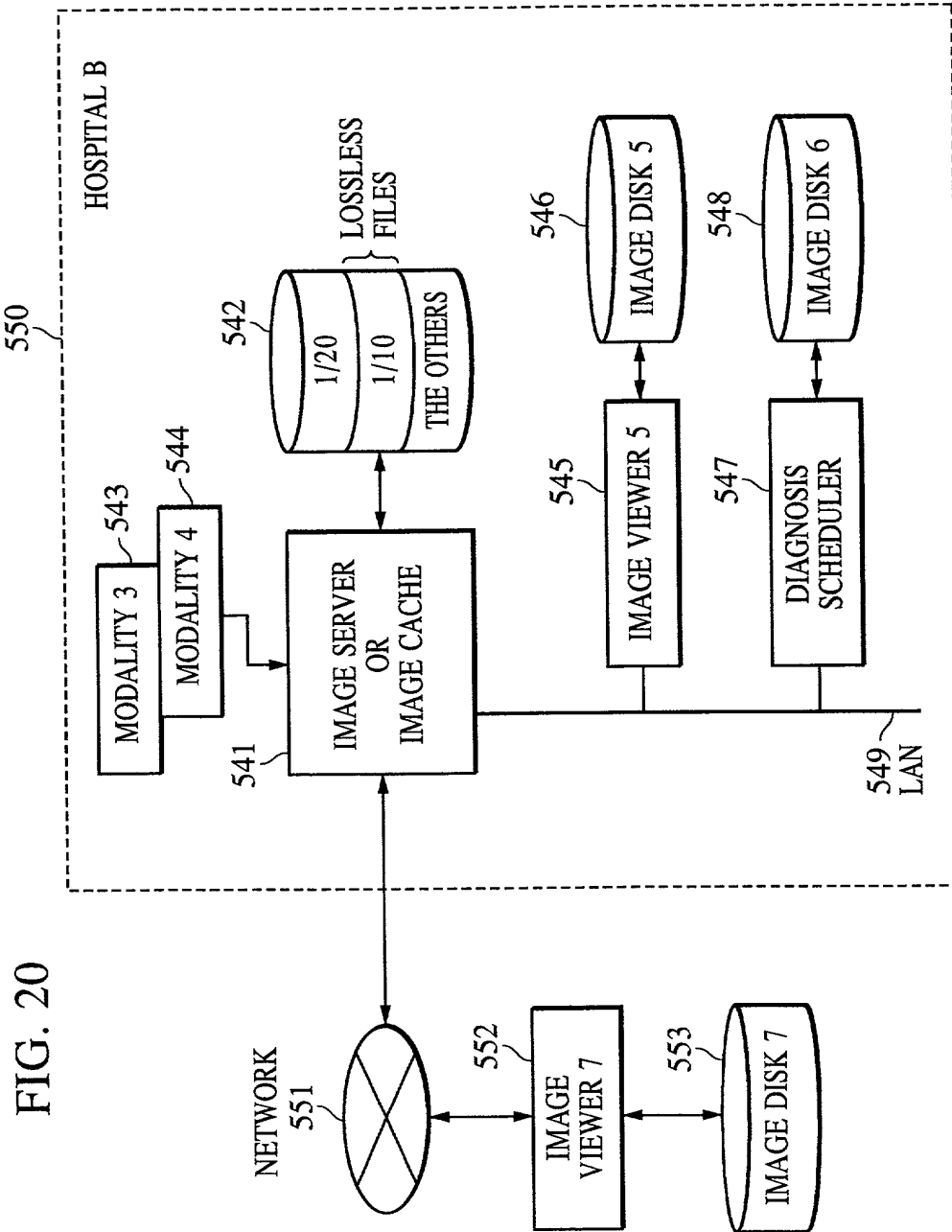


FIG. 21

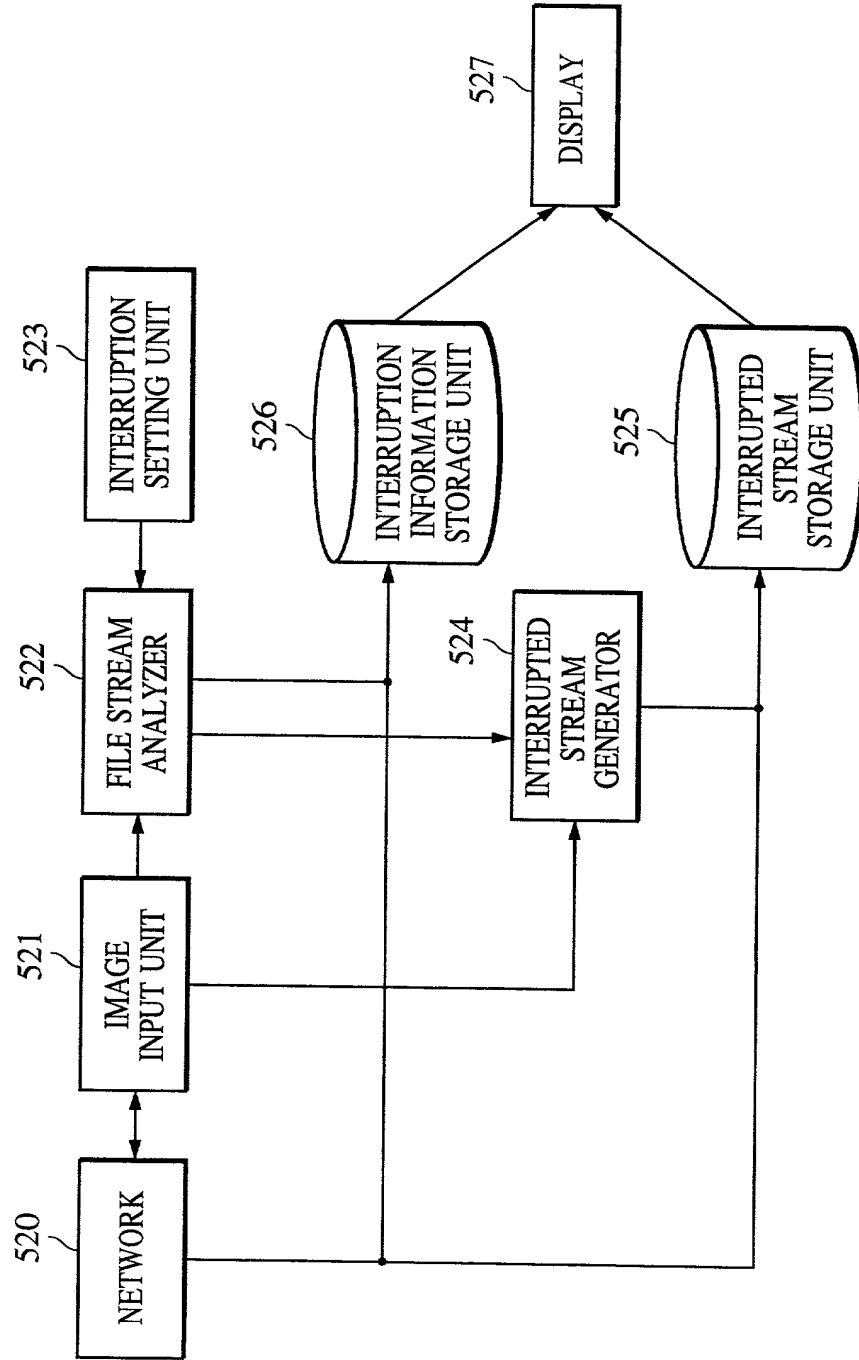


FIG. 22

